High Efficiency Switching Mode Battery Charger

General Description
The RT9535A is a PWM switch mode battery charger controller to fast charge single or multiple Li-Ion, NiMH and NiCd batteries, using constant current or constant voltage control. Maximum current can be easily programmed by external resistor. The constant voltage output can support up to 22V with 0.5% accuracy. A third control loop limits the input current drawing from the adapter during charging. This allows simultaneous operation of the equipment and fast battery charging without over loading to the adapter. The RT9535A can charge batteries from 2.5V to 22V with dropout voltage as low as 0.4V. The RT9535A is available in the WQFN-16L 4x4 package.

Features
- Fast Charging for Li-Ion, NiMH and NiCd Batteries
- Adjustable Battery Voltages from 2.5V to 22V
- High Efficiency: Up to 95%
- Charging Current Programmed by Resistor
- Precision 0.5% Charging Voltage Accuracy
- Provide 5% Charging Current Accuracy
- 500kHz Switching Frequency
- Auto Shutdown with Adapter Removal

Applications
- Notebook Computers
- Portable Instruments
- Chargers for Li-ion, NiMH, NiCd and Lead Acid Rechargeable Batteries

Simplified Application Circuit
Ordering Information

RT9535A

Package Type
QW : WQFN-16L 4x4 (W-Type)

Lead Plating System
G : Green (Halogen Free and Pb Free)

Note:
Richtek products are:

- RoHS compliant and compatible with the current requirements of IPC/JEDEC J-STD-020.
- Suitable for use in SnPb or Pb-free soldering processes

Marking Information

1Y = YM
DNN

1Y = : Product Code
YMDNN = Date Code

Pin Configurations

(TOP VIEW)

V5V
VIN
BOOT
HSD

EN
SS
ISET
VC

GND

NTC
VFB
VHH
BATT

BATT

SW
PGND
SNSH
SNSL

WQFN-16L 4x4
## Functional Pin Description

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Pin Name</th>
<th>Pin Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>EN</td>
<td>Enable Control Input (Active High). It must be connected to a logical voltage or pulled up to VIN with a 100kΩ resistor.</td>
</tr>
<tr>
<td>2</td>
<td>SS</td>
<td>Soft-Start Control Input. SS controls the soft-start time. Connect a capacitor from SS pin to GND to set the soft-start time.</td>
</tr>
<tr>
<td>3</td>
<td>ISET</td>
<td>Charge Current Setting and System Loop Compensation Pin. Connect a resistor from this pin to ground to set the charge current.</td>
</tr>
<tr>
<td>4</td>
<td>VC</td>
<td>Control Signal of the Inner Loop of the Current Mode PWM. A capacitor of at least 0.1μF with a serial resistor to GND filters out the current ripple.</td>
</tr>
<tr>
<td>5</td>
<td>NTC</td>
<td>Input for an external NTC thermistor for battery temperature monitoring.</td>
</tr>
<tr>
<td>6</td>
<td>VFB</td>
<td>Battery Voltage Feedback. Using an external resistor divider to set battery full charge voltage.</td>
</tr>
<tr>
<td>7</td>
<td>VHH</td>
<td>To supply the current sense amplifier CA for very low dropout condition. It must be connected as shown in the typical application circuit or connected to VIN if VIN is always larger than BATT by at least 1.8V.</td>
</tr>
<tr>
<td>8</td>
<td>BATT</td>
<td>Battery Voltage Sensing Input. A 10μF or larger X5R ceramic capacitor is recommended for filtering charge current ripple and stability purpose.</td>
</tr>
<tr>
<td>9</td>
<td>SNSL</td>
<td>Negative Terminal for Sensing Charge Current.</td>
</tr>
<tr>
<td>10</td>
<td>SNSH</td>
<td>Positive Terminal for Sensing Charge Current.</td>
</tr>
<tr>
<td>11</td>
<td>PGND</td>
<td>Power Ground.</td>
</tr>
<tr>
<td>12</td>
<td>SW</td>
<td>Switch Node. This pin switches between ground and VIN with high dv/dt rates. Care needs to be taken in the PCB layout to keep this node from coupling to other sensitive nodes.</td>
</tr>
<tr>
<td>13</td>
<td>HSD</td>
<td>Drain of Internal High-Side Power N-MOSFET Switch. Connect a low ESR capacitor of 10μF or higher from this pin to ground for good bypass.</td>
</tr>
<tr>
<td>14</td>
<td>BOOT</td>
<td>Bootstrap Supply for the High-Side Power Switch Gate Driver and Control Circuitry. In normal operation, ( V_{BOOT} = V_{SW} + 5V ).</td>
</tr>
<tr>
<td>15</td>
<td>VIN</td>
<td>Input Power Supply. Connect a low ESR capacitor of 10μF or higher from this pin to ground for good bypass.</td>
</tr>
<tr>
<td>16</td>
<td>V5V</td>
<td>Output of Internal 5V LDO. Connect a 1μF ceramic capacitor from this pin to GND for stability.</td>
</tr>
<tr>
<td>17</td>
<td>GND</td>
<td>Exposed Pad. Connect the exposed pad to PGND.</td>
</tr>
<tr>
<td></td>
<td>(Exposed Pad)</td>
<td></td>
</tr>
</tbody>
</table>
Function Block Diagram

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Operation
The RT9535A is a current mode PWM step-down switching charger controller. The battery DC charge current is programmed by a resistor R4 at the ISET pin and the ratio of sense resistor RS2 over RS1 in the typical application circuit. Amplifier CA converts the charge current through RS1 to a much lower sampled current \( I_{CHG} = I_{BATT} \times \frac{RS1}{RS2} \) fed into the ISET pin. Amplifier EA compares the output of CA with 2.5V reference voltage and drives the PWM loop to force them to be equal. Note that \( I_{CHG} \) has both AC and DC components. High DC accuracy is achieved with averaging filter R3 and C3 at ISET pin. \( I_{CHG} \) is mirrored to go through R4 and generates a ramp signal that is fed to the PWM control comparator, forming the current mode inner loop. An internal LDO generates a 5V to power high-side MOSFET gate driver. For batteries like lithium that require both constant current and constant voltage charging, the 0.5% 2.5V reference and the voltage amplifier VA reduce the charge current when battery voltage reaches the normal charge voltage level. For NiMH and NiCd, VA can be used for over-voltage protection.
Absolute Maximum Ratings  (Note 1)

- VHH, BOOT to GND                                           -0.3V to 36V
- VIN, EN, SW, HSD to GND                                      -0.3V to 30V
- ISET, VC, VFB, V5V SS, NTC to GND                           -0.3V to 6V
- BATT SNSH, SNSL to GND                                      -0.3V to 28V
- Power Dissipation,  $P_D @ T_A = 25^\circ C$
  WQFN-16L 4x4                                                3.5W
- Package Thermal Resistance  (Note 2)
  WQFN-16L 4x4, $R_{JA}$                                       28.5°C/W
  WQFN-16L 4x4, $R_{JC}$                                      7°C/W
- Lead Temperature (Soldering, 10 sec.)                        260°C
- Junction Temperature                                          150°C
- Storage Temperature Range                                    -65°C to 150°C
- ESD Susceptibility  (Note 3)
- HBM (Human Body Model)                                       2kV
- MM (Machine Model)                                            200V

Recommended Operating Conditions  (Note 4)

- Supply Input Voltage                                          4.5V to 28V
- Battery Voltage, VBAT                                          2.5V to 22V
- Ambient Temperature Range                                     -40°C to 85°C
- Junction Temperature Range                                    -40°C to 125°C

Electrical Characteristics
($V_IN = V_BAT + 3V$, $V_BAT$ is the full charge voltage, pull-up EN to VIN with 100kΩ resistor, $T_A = 25^\circ C$, unless otherwise specified)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Test Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Overall</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Supply Quiescent Current</td>
<td>$I_Q$</td>
<td>No Charge Current</td>
<td>0.5</td>
<td>1.3</td>
<td>2</td>
<td>mA</td>
</tr>
<tr>
<td>Supply Shutdown Current</td>
<td>$I_SD$</td>
<td>$V_{EN} = 0$</td>
<td>--</td>
<td>--</td>
<td>12</td>
<td>μA</td>
</tr>
<tr>
<td>Reverse Current from Battery</td>
<td>$I_{REV}$</td>
<td>$V_{IN}$ Floating, $V_{EN} = 0$</td>
<td>--</td>
<td>--</td>
<td>10</td>
<td>μA</td>
</tr>
<tr>
<td>VIN Under-Voltage Lockout</td>
<td>$V_{UVLO}$</td>
<td>$V_{IN}$ Floating, $V_{EN} = 0$</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VIN Under-Voltage Lockout Hysteresis</td>
<td>$V_{UVLO,HYS}$</td>
<td>$V_{IN}$ Floating, $V_{EN} = 0$</td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>Reference</td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>Reference Voltage</td>
<td>$V_{FB}$</td>
<td></td>
<td>2.486</td>
<td>2.5</td>
<td>2.514</td>
<td>V</td>
</tr>
<tr>
<td>FB Bias current</td>
<td>$I_{FB}$</td>
<td>$V_{FB} = 2.5V$</td>
<td>--</td>
<td>--</td>
<td>0.1</td>
<td>μA</td>
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</table>

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<thead>
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<th>Parameter</th>
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<th>Test Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
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<td>Charge Current</td>
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<tr>
<td>Full-Scale Charge Current</td>
<td>V_{ICHG}</td>
<td>Measure the Voltage Drop Across RS1</td>
<td>95</td>
<td>100</td>
<td>105</td>
<td>mV</td>
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<tr>
<td>Sense Voltage</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>ISET Output Current</td>
<td>I_{SET}</td>
<td>No Charge Current</td>
<td>--</td>
<td>--</td>
<td>--</td>
<td>mA</td>
</tr>
<tr>
<td>SNSL Bias Current</td>
<td>I_{SNSL}</td>
<td>No Charge Current</td>
<td>--36</td>
<td>--12</td>
<td>--6</td>
<td>µA</td>
</tr>
<tr>
<td>SNSH Bias Current</td>
<td>I_{SNSH}</td>
<td>No Charge Current</td>
<td>--36</td>
<td>--12</td>
<td>--6</td>
<td>µA</td>
</tr>
<tr>
<td>Battery Voltage</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>VHH Minimum Voltage with</td>
<td>ΔV_{HH}</td>
<td>(Note 5)</td>
<td>--</td>
<td>--</td>
<td>2</td>
<td>V</td>
</tr>
<tr>
<td>Respect to BATT</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VIN Minimum Voltage with</td>
<td>V_{DROP}</td>
<td>(Note 5)</td>
<td>--</td>
<td>0.3</td>
<td>0.4</td>
<td>V</td>
</tr>
<tr>
<td>Respect to BATT</td>
<td></td>
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<td></td>
<td></td>
<td></td>
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<tr>
<td>VHH Input Current</td>
<td>I_{VHH}</td>
<td>V_{HH} = 28V</td>
<td>40</td>
<td>95</td>
<td>150</td>
<td>µA</td>
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<tr>
<td>BATT Bias Current</td>
<td>I_{BATT}</td>
<td>V_{EN} = 0, V_{BATT} = V_{SW} =</td>
<td>--</td>
<td>--</td>
<td>10</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>V_{SNSH} = V_{SNSL} = 20V</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VC Pin Current</td>
<td>I_{VC}</td>
<td>V_{VC} = 0V</td>
<td>--25</td>
<td>--15</td>
<td>--1</td>
<td>µA</td>
</tr>
<tr>
<td>Switch Characteristics</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>Switching Frequency</td>
<td>f_{OSC}</td>
<td></td>
<td>430</td>
<td>500</td>
<td>545</td>
<td>kHz</td>
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<tr>
<td>High-Side Switch On-Resistance</td>
<td>R_{ON}</td>
<td></td>
<td>--</td>
<td>150</td>
<td>--</td>
<td>mΩ</td>
</tr>
<tr>
<td>High-Side Switch leakage</td>
<td>I_{HSD}</td>
<td>V_{HSD} = 28V, V_{EN} = 0V</td>
<td>--</td>
<td>--</td>
<td>10</td>
<td>µA</td>
</tr>
<tr>
<td>Current</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>BOOT Leakage Current</td>
<td>I_{BOOT}</td>
<td>V_{BOOT} = 30V, V_{EN} = 0V</td>
<td>--</td>
<td>1</td>
<td>--</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(Note 5)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Maximum Duty</td>
<td></td>
<td></td>
<td>95</td>
<td>--</td>
<td>--</td>
<td>%</td>
</tr>
<tr>
<td>SW Leakage Current</td>
<td>I_{LKGL}</td>
<td>V_{SW} = 28V, V_{EN} = 0V</td>
<td>--</td>
<td>--</td>
<td>10</td>
<td>µA</td>
</tr>
<tr>
<td>Regulator and Logic</td>
<td></td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>Characteristics</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LDO Output Voltage</td>
<td>V_{LDO}</td>
<td>50mA Load at V_{SV}, V_{VC} = 0V</td>
<td>4</td>
<td>5</td>
<td>6</td>
<td>V</td>
</tr>
<tr>
<td>EN Input Voltage</td>
<td>V_{EN}</td>
<td>Logic-High</td>
<td>2.5</td>
<td>--</td>
<td>--</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>V_{ENL}</td>
<td>Logic-Low</td>
<td>--</td>
<td>--</td>
<td>0.6</td>
<td></td>
</tr>
<tr>
<td>EN Input Current</td>
<td>I_{EN}</td>
<td>0V ≤ V_{EN} ≤ 5V</td>
<td>--</td>
<td>--</td>
<td>10</td>
<td>µA</td>
</tr>
<tr>
<td>Soft-Start Sourcing Current</td>
<td>I_{SS}</td>
<td></td>
<td>1.5</td>
<td>3.3</td>
<td>6</td>
<td>µA</td>
</tr>
<tr>
<td>Thermal Comparator and</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Protection</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>NTC Threshold, Cold</td>
<td>V_{COLD}</td>
<td>NTC Voltage Rising, 1% Hysteresis</td>
<td>73.5%</td>
<td>75%</td>
<td>76.5%</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>×V_{SV}</td>
<td>×V_{SV}</td>
<td>×V_{SV}</td>
<td>×V_{SV}</td>
<td></td>
</tr>
<tr>
<td>NTC Threshold, Hot</td>
<td>V_{HOT}</td>
<td>NTC Voltage Rising, 1% Hysteresis</td>
<td>31%</td>
<td>32.5%</td>
<td>34%</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>×V_{SV}</td>
<td>×V_{SV}</td>
<td>×V_{SV}</td>
<td>×V_{SV}</td>
<td></td>
</tr>
<tr>
<td>NTC Disable Threshold</td>
<td>V_{DISNTC}</td>
<td>NTC Voltage Rising, 1% Hysteresis</td>
<td>0.2%</td>
<td>1.7%</td>
<td>3.2%</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>×V_{SV}</td>
<td>×V_{SV}</td>
<td>×V_{SV}</td>
<td>×V_{SV}</td>
<td></td>
</tr>
<tr>
<td>NTC Bias Current</td>
<td>I_{NTC}</td>
<td></td>
<td>--</td>
<td>2</td>
<td>10</td>
<td>µA</td>
</tr>
<tr>
<td>Thermal Shutdown</td>
<td>T_{SD}</td>
<td>(Note 5)</td>
<td>--</td>
<td>160</td>
<td>--</td>
<td>°C</td>
</tr>
<tr>
<td>Temperature</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Thermal Shutdown Hysteresis</td>
<td>ΔT_{SD}</td>
<td>(Note 5)</td>
<td>--</td>
<td>30</td>
<td>--</td>
<td>°C</td>
</tr>
</tbody>
</table>
Note 1. Stresses listed as the above “Absolute Maximum Ratings” may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

Note 2. \( \theta_{JA} \) is measured at \( T_A = 25^\circ C \) on a high effective thermal conductivity four-layer test board per JEDEC 51-7. \( \theta_{JC} \) is measured at the exposed pad of the package.

Note 3. Devices are ESD sensitive. Handling precaution recommended.

Note 4. The device is not guaranteed to function outside its operating conditions.

Note 5. Guaranteed by design, not subjected to production test.
Typical Application Circuit

Note:
(1). For application with removable battery, a TVS with appropriate rating is required as shown above.
(2). $V_{IN} = 15V$ to $28V$, 3 – cell, $I_{CHARGE} = 1A$
Typical Operating Characteristics

Efficiency vs. Supply Voltage

- 1 Cell: $V_{BATT} = 4V$
- 2 Cell: $V_{BATT} = 8V$
- 3 Cell: $V_{BATT} = 12V$
- 4 Cell: $V_{BATT} = 16V$
- 5 Cell: $V_{BATT} = 20V$

Charge Current vs. Supply Voltage

- 1 Cell: $VIN = 12V, V_{BATT} = 4V$
- 2 Cell: $VIN = 24V, V_{BATT} = 8V$
- 3 Cell: $VIN = 24V, V_{BATT} = 12V$
- 4 Cell: $VIN = 24V, V_{BATT} = 16V$
- 5 Cell: $VIN = 24V, V_{BATT} = 20V$

Supply Current vs. Temperature

- VIN = 28V
- VIN = 12V

Shutdown Current vs. Temperature

- VIN = 28V
- VIN = 12V

V5V Voltage vs. Temperature

- VIN = 12V, $I_{V5V} = 40mA$
Charge Enable

\[
\begin{align*}
V_{BATT} &\ (2V/Div) \\
SW-GND &\ (10V/Div) \\
EN &\ (2V/Div) \\
I_{BATT} &\ (500mA/Div)
\end{align*}
\]

\[\text{Time (10ms/Div)}\]

Charge Disable

\[
\begin{align*}
V_{BATT} &\ (2V/Div) \\
SW-GND &\ (10V/Div) \\
EN &\ (2V/Div) \\
I_{BATT} &\ (500mA/Div)
\end{align*}
\]

\[\text{Time (10ms/Div)}\]

Switching

\[
\begin{align*}
I_{BATT} &\ (1A/Div) \\
V_{BATT} &\ (5V/Div) \\
I_{L} &\ (500mA/Div) \\
SW-GND &\ (10V/Div)
\end{align*}
\]

\[\text{Time (1\mu s/Div)}\]

BATT to GND Short Response

\[
\begin{align*}
I_{N} &\ (1A/Div) \\
V_{BATT} &\ (5V/Div) \\
I_{L} &\ (500mA/Div) \\
SW-GND &\ (10V/Div)
\end{align*}
\]

\[\text{Time (10ms/Div)}\]

\[\text{VIN} = 12V, V_{BATT} = 4V, I_{BATT} = 1A\]
Application Information

Input and Output Capacitors
In the typical application circuit, the input capacitor (C2) is assumed to absorb all input switching ripple current in the converter, so it must have adequate ripple current rating. Typically, at high charging currents, the converter will operate in continuous conduction mode. In this case, the RMS current $I_{RMS\text{IN}}$ of the input capacitor C2 can be estimated by the equation:

$$I_{RMS\text{IN}} = \frac{I_{BATT}}{\sqrt{D-D^2}}$$

Where $I_{BATT}$ is the battery charge current and D is the duty cycle. In worst case, the RMS ripple current will be equal to one half of output charging current at 50% duty cycle. For example, $I_{BATT} = 2$A, the maximum RMS current will be 1A. A low-ESR ceramic capacitor such as X7R or X5R is preferred for the input-decoupling capacitor and should be placed to the drain of the high-side MOSFET and source of the low-side MOSFET as close as possible. The voltage rating of the capacitor must be higher than the normal input voltage level. Above 20μF capacitance is suggested for typical of 2A charging current.

The output capacitor (C\text{BATT}) is also assumed to absorb output switching current ripple. The general formula for capacitor current is:

$$I_{RMS\text{CB}} = \frac{V_{\text{BATT}} \times (1 - \frac{V_{\text{BATT}}}{V_{\text{VIN}}})}{2 \times \sqrt{3} \times L1 \times f_{\text{OSC}} \times \Delta L}$$

For example, $V_{\text{VIN}} = 19$V, $V_{\text{BATT}} = 8.4$V, $L1 = 10$μH, and $f_{\text{OSC}} = 500$kHz, $I_{RMS} = 0.15$A.

EMI considerations usually make it desirable to minimize ripple current in the battery leads. Beads or inductors may be added to increase battery impedance at the 500kHz switching frequency. Switching ripple current splits between the battery and the output capacitor depending on the ESR of the output capacitor and the battery impedance. If the ESR of C\text{OUT} is 0.2Ω and the battery impedance is raised to 4Ω with a bead or inductor, only 5% of the ripple current will flow in the battery.

Inductor
The inductor value will be changed for more or less current ripple. The higher the inductance, the lower the current ripple will be. As the physical size is kept the same, typically, higher inductance will result in higher series resistance and lower saturation current. A good tradeoff is to choose the inductor so that the current ripple is approximately 30% to 50% of the full-scale charge current. The inductor value is calculated as :

$$L1 = \frac{V_{\text{BATT}} \times (V_{\text{VIN}} - V_{\text{BATT}})}{V_{\text{VIN}} \times f_{\text{OSC}} \times \Delta L}$$

Where $\Delta L$ is the inductor current ripple. For example, $V_{\text{VIN}} = 19$V, choose the inductor current ripple to be 40% of the full-scale charge current in the typical application circuit for 2A, 2-cell battery charger, $\Delta L = 0.8$A, $V_{\text{BATT}} = 8.4$V, calculate L1 to be 12μH. So choose L1 to be 10μH which is close to 12μH.

Soft-Start and Under-Voltage Lockout
The soft-start is controlled by the voltage rise time at SS pin. There are internal soft-start and external soft-start in the RT9535A. With a 0.01μF capacitor, time to reach full charge current is about 20ms and it is assumed that input voltage to the charger will reach full value in less than 20ms. The capacitor can be increased if longer input start-up times are needed.

For the RT9535A, it provides Under-Voltage Lockout (UVLO) protection. If 5V5LDO output voltage is lower than 3.5V, high-side internal power MOSFET. This will protect the adapter from entering a quasi "latch" state where the adapter output stays in a current limited state at reduced output voltage.
Full-Scale Charge Current Programming

The basic formula for full-scale charge current is (see Block Diagram):

\[ I_{\text{BATT}} = \frac{V_{\text{REF}}}{R_4} \times \frac{R_2}{R_1} \]

Where R4 is the total resistance from ISET pin to ground. For the sense amplifier CA biasing purpose, RS3 should have the same value as RS2 with 1% accuracy. For example, 2A full-scale charging current is needed. For low power dissipation on RS1 and enough signal to drive the amplifier CA, let RS1 = 100mV/2A = 50\,\Omega. This limits RS1 power to 0.2W. Let R4 = 10k\,\Omega, then:

\[ RS_2 = RS_3 = \frac{I_{\text{BATT}} \times R_4 \times R_1}{V_{\text{REF}}} = \frac{2A \times 10k \times 0.05}{2.5V} = 400\,\Omega \]

Note that for charge current accuracy and noise immunity, 100mV full scale level across the sense resistor RS1 is required. Consequently, both RS2 and RS3 should be 402\,\Omega. The R4 should be set to between 5k\,\Omega and 15k\,\Omega for the best operation.

It is critical to have a good Kelvin connection on the current sense resistor RS1 to minimize stray resistive and inductive pickup. RS1 should have low parasitic inductance (typical 3nH or less). The layout path from RS2 and RS3 to RS1 should be kept away from the fast switching SW node. A 1nF ceramic capacitor can be used across SNSH and SNSL and be kept away from the fast switching SW node.

Battery Voltage Regulation

The RT9535A uses high-accuracy voltage bandgap and regulator for the high charging-voltage accuracy. The charge voltage is programmed via a resistor divider from the battery to ground, with the midpoint tied to the VFB pin. The voltage at the VFB pin is regulated to 2.5V, giving the following equation for the regulation voltage:

\[ V_{\text{BATT}} = 2.5 \times \left(1 + \frac{R_2}{R_1}\right) \]

where RF2 is connected from VFB to the battery and RF1 is connected from VFB to GND.

Charging

The 2A Battery Charger (typical application circuit) charges lithium-ion batteries at a constant 2A until battery voltage reaches the setting value. The charger will then automatically go into a constant voltage mode with current decreasing to near zero over time as the battery reaches full charge.

Dropout Operation

The RT9535A can charge the battery even when VIN goes as low as 2V above the combined voltages of the battery and the drops on the sense resistor as well as parasitic wiring. This low VIN sometimes forces 100% duty cycle and high-side power switch stays on for many switching cycles. While high-side power switch stays on, the voltage \( V_{\text{BOOT}} \) across the capacitor C8 drops down slowly because the current sink at BOO'T pin. C8 needs to be recharged before \( V_{\text{BOOT}} \) drops too low to keep the topside switch on.

A unique design allows the RT9535A to operate under these conditions. If SW pin voltage keeps larger than 1.3V for 32 oscillation periods, high-side power MOSFET will be turned off and an internal MOSFET will be turned on to pull SW pin down. This function refreshes \( V_{\text{BOOT}} \) voltage to a higher value. It is important to use 0.1\,\mu F to hold \( V_{\text{BOOT}} \) up for a sufficient amount of time.

Shutdown

When adapter power is removed, VIN will drift down. As soon as VIN goes down to 0.1V above \( V_{\text{BATT}} \), the RT9535A will go into sleep mode drawing only \( \sim 10\,\mu A \) from the battery. There are two suggest ways to stop switching: pulling the EN pin low or pulling the VC pin low. Pulling the EN pin low will shut down the whole
chip. Pulling the VC pin low will only stop switching and 5V LDO stays active. Make sure there is a pull-up resistor on the EN pin even if the EN pin is not used, otherwise internal pull-down current will keep the EN pin low to shut down mode when power turns on.

**Charger Protection**

Note that the RT9535A will operate even when VBATT is grounded. If VBATT of typical application circuit charger gets shorted to ground very quickly from a high battery voltage, slow loop response may allow charge current to build up and damage the high-side internal N-MOSFET. A small diode from the EN pin to VBATT will shut down switching and protect the charger.

**Temperature Qualification**

The controller RT9535A continuously monitors battery temperature by measuring the voltage between the NTC pin and GND. A negative temperature coefficient thermistor (NTC) and an external voltage divider typically generate this voltage. The controller compares this voltage against its internal thresholds to determine if charging is allowed. To initiate a charge cycle, the battery temperature must be within the VCOLD. If battery temperature is outside of this range, the controller suspends charge and the safety timer and waits until the battery temperature is within the VCOLD to VHOT range. During the charge cycle, the battery temperature must be within the VCOLD and VDISNTC thresholds. If the battery temperature is outside of this range, the controller suspends charge and waits until the battery temperature is within the VCOLD to VHOT range. The controller suspends charge by turning off the PWM charge MOSFETs.

**Thermistor Sense Thresholds**

Assuming a 103AT NTC thermistor on the battery pack as shown in the below, the values of RT1 and RT2 can be determined by using the following equations:

\[
RT2 = \frac{1}{R_{TH,COLD} \times R_{TH,HOT} \times (\frac{V_{V5V}}{V_{V5V,HOT}} - 1)} - \frac{1}{V_{V5V}}
\]

\[
RT1 = \frac{1}{1 + \frac{1}{RT2} \times R_{TH,COLD}}
\]

Where \(R_{TH,COLD}\) and \(R_{TH,HOT}\) which have defined in the spec of the 103AT NTC thermistor.

**Thermal Considerations**

For continuous operation, do not exceed absolute maximum junction temperature. The maximum power dissipation depends on the thermal resistance of the IC package, PCB layout, rate of surrounding airflow, and difference between junction and ambient temperature. The maximum power dissipation can be calculated by the following formula:

\[
P_{D(MAX)} = (T_{J(MAX)} - T_A) / \theta_{JA}
\]
where $T_{J(MAX)}$ is the maximum junction temperature, $T_A$ is the ambient temperature, and $\theta_{JA}$ is the junction to ambient thermal resistance.

For recommended operating condition specifications, the maximum junction temperature is 125°C. The junction to ambient thermal resistance, $\theta_{JA}$, is layout dependent. For WQFN-16L 4x4 package, the thermal resistance, $\theta_{JA}$, is 28.5°C/W on a standard JEDEC 51-7 four-layer thermal test board. The maximum power dissipation at $T_A = 25°C$ can be calculated by the following formula:

$$P_{D(MAX)} = \frac{(125°C - 25°C)}{(28.5°C/W)} = 3.5W$$

for WQFN-16L 4x4 package.

The maximum power dissipation depends on the operating ambient temperature for fixed $T_{J(MAX)}$ and thermal resistance, $\theta_{JA}$. The derating curve in Figure 1 allows the designer to see the effect of rising ambient temperature on the maximum power dissipation.

![Figure 1. Derating Curve of Maximum Power Dissipation](image-url)
Layout Consideration

Switch rise and fall times are under 20ns for maximum efficiency. To prevent radiation, the SW pin, the rectifier Schottky diode D1 and input bypass capacitor leads should be kept as short as possible. A ground plane should be used under the switching circuitry to prevent inter-plane coupling and to act as a thermal spreading path. Note that the rectifier Schottky diode D1 is probably the most heat dissipating device in the charging system. The voltage drop on a 2A Schottky diode can be 0.5V. With 50% duty cycle, the power dissipation can go as high as 0.5W. Expanded traces should be used for the diode leads for low thermal resistance. Another large heat dissipating device is probably the inductor. The fast switching high current ground path including the MOSFETs, D1 and input bypass capacitor C2 should be kept very short. Another smaller input bypass (1μF ceramic or larger paralleled with C3) should be placed to VIN pin and GND pin as close as possible.

Input capacitor and C7 must be placed as close to the IC as possible.

Place these power components as close to the SW pin as possible.

Locate the compensation components to the SS/VC/ISET pin as close as possible.

G10 and C11 must be placed as close to the IC as possible.

Figure 2. PCB Layout Guide
### Outline Dimension

#### Symbol
- **A**: 0.700 - 0.800 mm / 0.028 - 0.031 inch
- **A1**: 0.000 - 0.050 mm / 0.000 - 0.002 inch
- **A3**: 0.175 - 0.250 mm / 0.007 - 0.010 inch
- **b**: 0.250 - 0.380 mm / 0.010 - 0.015 inch
- **D**: 3.950 - 4.050 mm / 0.156 - 0.159 inch
- **D2**: 2.000 - 2.450 mm / 0.079 - 0.096 inch
- **E**: 3.950 - 4.050 mm / 0.156 - 0.159 inch
- **E2**: 2.000 - 2.450 mm / 0.079 - 0.096 inch
- **e**: 0.650 mm / 0.026 inch
- **L**: 0.500 - 0.600 mm / 0.020 - 0.024 inch

#### W-Type 16L QFN 4x4 Package

Note: The configuration of the Pin #1 identifier is optional, but must be located within the zone indicated.