High Voltage Multi-Topology LED Driver

General Description
The RT8463 is a current mode PWM regulator for LED driving applications. With a 2A power switch, wide input voltage (4.5V to 50V) and output voltage (up to 50V) ranges, the RT8463 can operate in any of the three common topologies: Buck, Boost or Buck-Boost.

With 470kHz operating frequency, the size of the external PWM inductor and input/output capacitors can be minimized. High efficiency is achieved by a 100mV current sensing control.

Brightness dimming can be controlled from either analog or PWM signal. A unique built-in clamping comparator and filtering resistor allow easy low noise analog dimming conversion from PWM signal with only one external capacitor.

The RT8463 is available in the TSSOP-14 (Exposed pad) and WDFN-12L 3x3 packages.

Features
- High Voltage: VIN Up to 50V, VOUT Up to 50V
- Buck, Boost or Buck-Boost Operation
- Built-In 2A Power Switch
- Current Mode PWM Control
- 470kHz Fixed Switching Frequency
- Easy Dimming: Analog, PWM Digital or PWM Converting to Analog with One External Capacitor
- Adjustable Soft-Start to Avoid Inrush Current
- Adjustable Over Voltage Protection to Limit Output Voltage
- Thermal Shutdown
- Under Voltage Lockout
- RoHS Compliant and Halogen Free

Applications
- GPS, Portable DVD Backlight
- Desk Lights and Room Lighting
- Industrial Display Backlight

Ordering Information
RT8463

<table>
<thead>
<tr>
<th>Package Type</th>
<th>Lead Plating System</th>
</tr>
</thead>
<tbody>
<tr>
<td>CP : TSSOP-14 (Exposed Pad-Option 1)</td>
<td>G : Green (Halogen Free and Pb Free)</td>
</tr>
<tr>
<td>QW : WDFN-12L 3x3 (W-Type) (Exposed Pad-Option 1)</td>
<td></td>
</tr>
</tbody>
</table>

Note:
Richtek products are:
- RoHS compliant and compatible with the current requirements of IPC/JEDEC J-STD-020.
- Suitable for use in SnPb or Pb-free soldering processes.

Marking Information
RT8463GCP

RT8463GCP : Product Number
YMDNN : Date Code

98 = : Product Code
YMDNN : Date Code

Simplified Application Circuit
Pin Configuration

(TOP VIEW)

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Pin Name</th>
<th>Pin Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>TSSOP-14 (Exposed Pad)</td>
<td>WDFN-12L 3x3</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>ISP</td>
<td>Positive current sense input.</td>
</tr>
<tr>
<td>2</td>
<td>ISN</td>
<td>Negative current sense input. Voltage threshold between ISP and ISN is 100mV.</td>
</tr>
<tr>
<td>3</td>
<td>VC</td>
<td>Compensation node for PWM boost converter loop.</td>
</tr>
<tr>
<td>4</td>
<td>ACTL</td>
<td>Analog dimming control input. Effective programming range is between 0.2V and 1.2V.</td>
</tr>
<tr>
<td>5</td>
<td>DCTL</td>
<td>Digital dimming control input. By adding a 0.47μF filtering capacitor on the ACTL pin, the PWM dimming signal on DCTL pin will be averaged and converted into analog dimming signal on the ACTL pin. VACTL = 1.2V x PWM dimming duty cycle.</td>
</tr>
<tr>
<td>6</td>
<td>EN</td>
<td>Enable control input (active high). When this pin is low, the chip is in shutdown mode.</td>
</tr>
<tr>
<td>7, 8, 11, 15 (Exposed Pad)</td>
<td>9, 13 (Exposed Pad)</td>
<td>GND</td>
</tr>
<tr>
<td>9</td>
<td>OVP</td>
<td>Over voltage protection sense input. The PWM Boost converter turns off when VOVP goes higher than 1.2V.</td>
</tr>
<tr>
<td>10</td>
<td>SS</td>
<td>Soft-start time setting. A minimum 10nF capacitor is required for soft-start.</td>
</tr>
<tr>
<td>12</td>
<td>SW</td>
<td>Switch node of PWM boost converter.</td>
</tr>
<tr>
<td>13</td>
<td>CREG</td>
<td>Regulator output for internal circuit. Placed a 1μF capacitor to stabilize the 5V output regulator.</td>
</tr>
<tr>
<td>14</td>
<td>VCC</td>
<td>Power supply voltage input. For good bypass, a low ESR capacitor is required.</td>
</tr>
</tbody>
</table>
Operation

The RT8463 is specifically designed to be operated in Buck, Boost and Buck-Boost converter applications. This device uses a fixed frequency, current mode control scheme to provide excellent line and load regulations. The control loop has a current sense amplifier to sense the voltage between the ISP and ISN pins and provides an output voltage at the VC pin. A PWM comparator then turns off the internal power switch when the sensed power switch current exceeds the compensated VC pin voltage. The power switch will not reset by the oscillator clock in each cycle. If the comparator does not turn off the switch in a cycle, the power switch is on for more than a full switching period until the comparator is tripped. In this manner, the programmed voltage across the sense resistor is regulated by the control loop.

The current through the sense resistor is set by the programmed voltage and the sense resistance. The voltage across the sense resistor can be programmed by either the analog or PWM signals at the ACTL pin, or the PWM signal at the DCTL pin.

The RT8463 provides protection functions which include over temperature, input voltage under voltage, output voltage over voltage, and switch current limit.
Absolute Maximum Ratings  (Note 1)

- Supply Input Voltage, VCC: −0.3V to 60V
- SW Pin Voltage at Switching Off, ISP, ISN: −0.3V to 60V
- DCTL, ACTL, CREG, OVP Pin Voltage: −0.3V to 5.5V  (Note 2)
- EN Pin Voltage: −0.3V to 20V
- Power Dissipation, PD @ TA = 25°C
  - TSSOP-14 (Exposed Pad): 3.32W
  - WDFN-12L 3x3: 3.28W
- Package Thermal Resistance  (Note 3)
  - TSSOP-14 (Exposed Pad), θJA: 30.1°C/W
  - TSSOP-14 (Exposed Pad), θJC: 7.5°C/W
  - WDFN-12L 3x3, θJA: 30.5°C/W
  - WDFN-12L 3x3, θJC: 7.5°C/W
- Junction Temperature: 150°C
- Lead Temperature (Soldering, 10 sec.): 260°C
- Storage Temperature Range: −65°C to 150°C
- ESD Susceptibility  (Note 4)
  - HBM (Human Body Model): 2kV
  - MM (Machine Model): 200V

Recommended Operating Conditions  (Note 5)

- Supply Input Voltage, VCC: 4.5V to 50V
- Junction Temperature Range: −40°C to 125°C
- Ambient Temperature Range: −40°C to 85°C

Electrical Characteristics

(VCC = 12V, No Load on any Output, TA = 25°C, unless otherwise specified)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Test Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Overall</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Regulator Output Voltage</td>
<td>VCREG</td>
<td>VCREG = 20mA</td>
<td>4.5</td>
<td>5</td>
<td>5.5</td>
<td>V</td>
</tr>
<tr>
<td>Supply Current</td>
<td>IVCC</td>
<td>VC ≤ 0.2V (Not Switching )</td>
<td>--</td>
<td>--</td>
<td>5</td>
<td>mA</td>
</tr>
<tr>
<td>VIN Under Voltage Lockout Threshold</td>
<td>VUVLO</td>
<td>VIN Rising</td>
<td>--</td>
<td>4.2</td>
<td>--</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>VIN Falling</td>
<td>--</td>
<td>3.8</td>
<td>--</td>
<td></td>
</tr>
<tr>
<td>Shutdown Current</td>
<td>ISHDN</td>
<td>VEN &lt; 0.5V</td>
<td>--</td>
<td>--</td>
<td>15</td>
<td>μA</td>
</tr>
<tr>
<td>EN Input Voltage</td>
<td>VEN_H</td>
<td></td>
<td>2</td>
<td>--</td>
<td>--</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>VEN_L</td>
<td></td>
<td>--</td>
<td>--</td>
<td>0.5</td>
<td></td>
</tr>
<tr>
<td>EN Input Current</td>
<td>VEN</td>
<td></td>
<td>--</td>
<td>--</td>
<td>1</td>
<td>μA</td>
</tr>
<tr>
<td>Current Sense Amplifier</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input Threshold (VISP − VSN)</td>
<td>VACTL</td>
<td>≥ 1.25V</td>
<td>96</td>
<td>100</td>
<td>102</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>= 1.2V</td>
<td>95</td>
<td>98</td>
<td>101</td>
<td></td>
</tr>
<tr>
<td>Parameter</td>
<td>Symbol</td>
<td>Test Conditions</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
<td>Unit</td>
</tr>
<tr>
<td>----------------------------------------------</td>
<td>--------</td>
<td>--------------------------</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
<td>------</td>
</tr>
<tr>
<td>Input Current</td>
<td>I_{ISP}</td>
<td>V_{ISP} = 24V</td>
<td>--</td>
<td>200</td>
<td>--</td>
<td>μA</td>
</tr>
<tr>
<td>Input Current</td>
<td>I_{ISN}</td>
<td>V_{ISN} = 24V</td>
<td>--</td>
<td>20</td>
<td>--</td>
<td>μA</td>
</tr>
<tr>
<td>Output Current</td>
<td>I_{VC}</td>
<td>2V &gt; VC &gt; 0.2V</td>
<td>--</td>
<td>±10</td>
<td>--</td>
<td>μA</td>
</tr>
<tr>
<td>VC Threshold for PWM Switch Off</td>
<td></td>
<td></td>
<td>--</td>
<td>0.2</td>
<td>--</td>
<td>V</td>
</tr>
</tbody>
</table>

### LED Dimming

- **Analog Dimming ACTL Pin Input Current**
  \[ I_{ACTL} \quad 0 \leq V_{ACTL} \leq 3V, \text{DCTL Floating} \]
  Min: -- Typ: -- Max: 2 μA

- **LED Current On Threshold at ACTL**
  \[ V_{ACTL\_ON} \quad (V_{ISP} - V_{ISN}) = 100mV \]
  Min: -- Typ: 1.2 Max: 1.33 V

- **LED Current Off Threshold at ACTL**
  \[ V_{ACTL\_OFF} \]
  Min: -- Typ: 0.2 Max: 0.25 V

- **DCTL Input Current**
  \[ I_{DCTL} \quad 0.3V \leq V_{DCTL} \leq 5V \]
  Min: -- Typ: 0.5 Max: 2 μA

- **DCTL Input Voltage**
  | Logic-High | V_{DCTL\_H} | 2 | -- | -- | V |
  | Logic-Low  | V_{DCTL\_L} | -- | -- | 0.1 | V |

### PWM Boost Converter

- **Switching Frequency**
  \[ f_{SW} \]
  Min: 420 Typ: 470 Max: 520 kHz

- **Maximum Duty Cycle**
  \[ D_{MAX} \]
  Min: -- Typ: -- Max: 100 %

- **Minimum On-Time (Note 6)**
  \[ SW \]
  Min: -- Typ: 0.3 Max: 250 ns

- **SW R_{DS(ON)}**
  \[ SW \]
  Min: -- Typ: 0.3 Max: 0.5 Ω

- **SW Current Limit**
  \[ I_{LIM\_SW} \]
  Min: 2 Typ: 2.5 Max: -- A

### OVP and Soft-Start

- **OVP Threshold**
  \[ V_{OVP} \]
  Min: 1.15 Typ: 1.2 Max: 1.25 V

- **OVP Input Current**
  \[ I_{OVP} \quad V_{OVP} \leq 1.5V \]
  Min: -- Typ: -- Max: 50 nA

- **Soft-Start SS Pin Current**
  \[ I_{SS} \quad V_{SS} \leq 2.5V \]
  Min: -- Typ: 5 Max: 8 μA

### Temperature Protection

- **Thermal Shutdown Temperature**
  \[ T_{SD} \]
  Min: -- Typ: 150 Max: -- °C

- **Thermal Shutdown Hysteresis**
  \[ \Delta T_{SD} \]
  Min: -- Typ: 20 Max: -- °C

**Note 1.** Stresses beyond those listed “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions may affect device reliability.

**Note 2.** If connected with a 20kΩ serial resistor, ACTL and DCTL can go up to 40V.

**Note 3.** \( \theta_{JA} \) is measured under natural convection (still air) at \( T_A = 25^\circ \text{C} \) with the component mounted on a high effective-thermal-conductivity four-layer test board on a JEDEC 51-7 thermal measurement standard. \( \theta_{JC} \) is measured at the exposed pad of the package.

**Note 4.** Devices are ESD sensitive. Handling precaution is recommended.

**Note 5.** The device is not guaranteed to function outside its operating conditions.

**Note 6.** Guaranteed by design, not subjected to production test.
Typical Application Circuit

Figure 2. PWM to Analog Dimming Buck Configuration

Figure 3. Analog Dimming Buck Configuration

Figure 4. PWM Dimming Buck Configuration Through ACTL Pin
Figure 5. PWM to Analog Dimming Boost Configuration

Figure 6. PWM to Analog Dimming Buck-Boost Configuration
**Typical Operating Characteristics**

- **Supply Current vs. VCC**
- **Shutdown Current vs. VCC**
- **Efficiency vs. Input Voltage**
- **Supply Current vs. Temperature**
- **ISP-ISN Threshold vs. Temperature**

### Supply Current vs. VCC

![Graph](supply_current_vs_vcc.png)

- **Y-axis:** Supply Current (mA)
- **X-axis:** VCC (V)

### Shutdown Current vs. VCC

![Graph](shutdown_current_vs_vcc.png)

- **Y-axis:** Shutdown Current (μA)
- **X-axis:** VCC (V)

### Efficiency vs. Input Voltage

- **Boost:**
  - **Y-axis:** Efficiency (%)
  - **X-axis:** Input Voltage (V)
  - **Note:** $V_{OUT} = 30V, I_{OUT} = 210mA$

- **Buck:**
  - **Y-axis:** Efficiency (%)
  - **X-axis:** Input Voltage (V)
  - **Note:** $V_{OUT} = 15V, I_{OUT} = 210mA$

### Supply Current vs. Temperature

![Graph](supply_current_vs_temperature.png)

- **Y-axis:** Supply Current (mA)
- **X-axis:** Temperature (°C)
- **Note:** $V_{CC} = 12V$

### ISP-ISN Threshold vs. Temperature

![Graph](isp_isn_threshold_vs_temperature.png)

- **Y-axis:** ISP-ISN Threshold (mV)
- **X-axis:** Temperature (°C)
- **Note:** $V_{IN} = 12V$
Power On from VIN

- \( I_{OUT} \) (200mA/Div)
- \( V_{OUT} \) (20V/Div)
- \( V_{IN} \) (10V/Div)
- Time (25ms/Div)

Power Off from VIN

- \( I_{OUT} \) (200mA/Div)
- \( V_{OUT} \) (20V/Div)
- \( V_{IN} \) (10V/Div)
- Time (250ms/Div)
Application Information

Loop Compensation
The RT8463 has an external compensation pin (VC) allowing the loop response optimized for specific application. An external resistor in series with a capacitor is connected from the VC pin to GND to provide a pole and a zero for proper loop compensation. The recommended compensation resistance and capacitance for the RT8463 are 10kΩ and 3.3nF.

Soft-Start
The soft-start can be achieved by connecting a capacitor from the SS pin to GND. The built-in soft-start circuit reduces the start-up current spike and output voltage overshoot. The soft-start time is determined by the external capacitor charged by an internal 5μA constant charging current. The SS pin directly limits the slew rate of voltage on the VC pin, which in turn limits the peak switch current. The value of the soft-start capacitor is user defined to satisfy the designer's requirements.

LED Current Setting
The LED current could be calculated by the following equation:

\[ I_{\text{LED(MAX)}} = \frac{V_{(\text{ISP} - \text{ISN})}}{R_2} \]

where \( V_{(\text{ISP} - \text{ISN})} \) is the voltage between ISP and ISN (100mV typ. if ACTL or DCTL dimming is not applied) and the \( R_2 \) is the resistor between ISP and ISN.

Brightness / Dimming Control
The RT8463 features both analog and digital dimming control. Analog dimming is linearly controlled by an external voltage (0.2V < \( V_{\text{ACTL}} < 1.2V \)). With an on-chip output clamping amplifier and a resistor, PWM dimming signal fed at ACTL pin can be easily filtered to an analog dimming signal with an external capacitor from the ACTL pin to GND for noise-free PWM dimming. A very high contrast ratio true digital PWM dimming can be achieved by driving the ACTL pin with a PWM signal from 100Hz to 10kHz.

Output Over Voltage Setting
The RT8463 is equipped with Over Voltage Protection (OVP) function. When the voltage at OVP pin exceeds a threshold of approximately 1.2V, the power switch is turned off. The power switch can be turned on again once the voltage at OVP pin drops below 1.2V.

For the Boost application, the output voltage could be clamped at a certain voltage level. The OVP voltage can be set by the following equation:

\[ V_{\text{OUT,OVP}} = 1.2 \times \left(1 + \frac{R_3}{R_4}\right) \]

where \( R_3 \) and \( R_4 \) are the voltage divider from \( V_{\text{OUT}} \) to GND with the divider center node connected to the OVP pin.

Current Limit Protection
The RT8463 can limit the peak switch current by the internal over current protection feature. In normal operation, the power switch is turned off when the switch current reaches the loop-set value. The maximum peak-current limit of the switch is 2.5A (typ.).

Over Temperature Protection
The RT8463 provides Over Temperature Protection (OTP) function to prevent the excessive power dissipation from overheating. The OTP function will shut down switching operation when the die junction temperature exceeds 150°C. The chip will automatically start to switch again when the die junction temperature cools off.

Inductor Selection
Choose an inductor that can handle the necessary peak current without saturating, and ensure that the inductor has a low DCR (copper wire resistance) to minimize \( I^2R \) power losses. Inductor manufacturers specify the maximum current rating as the current where the inductance falls to certain percentage of its nominal value (65% typ.).
Schottky Diode Selection

The Schottky diode, with low forward voltage drop and fast switching speed, is necessary for the RT8463 applications. In addition, power dissipation, reverse voltage rating and pulsating peak current are the important parameters of the Schottky diode that must be considered. Choose a suitable Schottky diode whose reverse voltage rating is greater than the maximum output voltage. The diode's average current rating must exceed the average output current. The diode conducts current only when the power switch is turned off (typically less than 50% duty cycle).

Capacitor Selection

The input capacitor reduces current spikes from the input supply and minimizes noise injection to the converter. For most RT8463 applications, a 4.7μF ceramic capacitor is sufficient. A higher or lower value may be used depending on the noise level from the input supply and the input current to the converter.

In Boost application, the output capacitor is typically a ceramic capacitor and is selected based on the output voltage ripple requirements. The minimum value of the output capacitor, C_{OUT}, is approximately given by the following equation:

\[ C_{OUT} = \frac{LED \times D \times T}{V_{RIPPLE}} \]
Thermal Considerations
The junction temperature should never exceed the absolute maximum junction temperature $T_{J(MAX)}$, listed under Absolute Maximum Ratings, to avoid permanent damage to the device. The maximum allowable power dissipation depends on the thermal resistance of the IC package, the PCB layout, the rate of surrounding airflow, and the difference between the junction and ambient temperatures. The maximum power dissipation can be calculated using the following formula:

$$P_{D(MAX)} = \frac{(T_{J(MAX)} - T_A)}{\theta_{JA}}$$

where $T_{J(MAX)}$ is the maximum junction temperature, $T_A$ is the ambient temperature, and $\theta_{JA}$ is the junction-to-ambient thermal resistance.

For continuous operation, the maximum operating junction temperature indicated under Recommended Operating Conditions is 125°C. The junction-to-ambient thermal resistance, $\theta_{JA}$, is highly package dependent. For a TSSOP-14 (Exposed Pad) package, the thermal resistance, $\theta_{JA}$, is 30.1°C/W on a standard JEDEC 51-7 high effective-thermal-conductivity four-layer test board. For a WDFN-12L 3x3 package, the thermal resistance, $\theta_{JA}$, is 30.5°C/W on a standard JEDEC 51-7 high effective-thermal-conductivity four-layer test board. The maximum power dissipation at $T_A = 25°C$ can be calculated as below:

$$P_{D(MAX)} = \frac{(125°C - 25°C)}{(30.1°C/W)} = 3.32W$$ for a TSSOP-14 (Exposed Pad) package.

$$P_{D(MAX)} = \frac{(125°C - 25°C)}{(30.5°C/W)} = 3.28W$$ for a WDFN-12L 3x3 package.

The maximum power dissipation depends on the operating ambient temperature for the fixed $T_{J(MAX)}$ and the thermal resistance, $\theta_{JA}$. The derating curves in Figure 7 allows the designer to see the effect of rising ambient temperature on the maximum power dissipation.

![Figure 7. Derating Curve of Maximum Power Dissipation](image)

Layout Consideration
PCB layout is very important to design power switching converter circuits. The recommended layout guidelines are listed as follows:

- The power components L1, D1, $C_{VIN}$, and $C_{OUT}$ must be placed as close to each other as possible to reduce the ac current loop area. The PCB trace between power components must be as short and wide as possible due to large current flow through these traces during operation.
- Place L1 and D1 connected to SW pin as close as possible. The trace should be as short and wide as possible.
- The input capacitors $C1$ must be placed as close to $VCC$ pin as possible.
- Place the compensation components to the VC pin as close as possible to avoid noise pick up.
Figure 8. PCB Layout Guide for WDFN-12L 3x3

Place these components as close as possible.

Locate the compensation components to VC pin as close as possible.

Locate input capacitor to VCC as close as possible.
### Outline Dimension

**14-Lead TSSOP (Exposed Pad) Plastic Package**

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Dimensions In Millimeters</th>
<th>Dimensions In Inches</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Min</td>
<td>Max</td>
</tr>
<tr>
<td>A</td>
<td>1.000</td>
<td>1.200</td>
</tr>
<tr>
<td>A1</td>
<td>0.000</td>
<td>0.150</td>
</tr>
<tr>
<td>A2</td>
<td>0.800</td>
<td>1.050</td>
</tr>
<tr>
<td>b</td>
<td>0.190</td>
<td>0.300</td>
</tr>
<tr>
<td>D</td>
<td>4.900</td>
<td>5.100</td>
</tr>
<tr>
<td>e</td>
<td>0.650</td>
<td></td>
</tr>
<tr>
<td>E</td>
<td>6.300</td>
<td>6.500</td>
</tr>
<tr>
<td>E1</td>
<td>4.300</td>
<td>4.500</td>
</tr>
<tr>
<td>L</td>
<td>0.450</td>
<td>0.750</td>
</tr>
<tr>
<td>U</td>
<td>Option1</td>
<td>1.900</td>
</tr>
<tr>
<td></td>
<td>Option2</td>
<td>2.350</td>
</tr>
<tr>
<td></td>
<td>Option3</td>
<td>2.640</td>
</tr>
<tr>
<td>V</td>
<td>Option1</td>
<td>1.600</td>
</tr>
<tr>
<td></td>
<td>Option2</td>
<td>2.250</td>
</tr>
<tr>
<td></td>
<td>Option3</td>
<td>2.550</td>
</tr>
</tbody>
</table>
Note: The configuration of the Pin #1 identifier is optional, but must be located within the zone indicated.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Dimensions in Millimeters</th>
<th>Dimensions in Inches</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>Min. 0.700 Max. 0.800</td>
<td>Min. 0.028 Max. 0.031</td>
</tr>
<tr>
<td>A1</td>
<td>Min. 0.000 Max. 0.050</td>
<td>Min. 0.000 Max. 0.002</td>
</tr>
<tr>
<td>A3</td>
<td>Min. 0.175 Max. 0.250</td>
<td>Min. 0.007 Max. 0.010</td>
</tr>
<tr>
<td>b</td>
<td>Min. 0.150 Max. 0.250</td>
<td>Min. 0.006 Max. 0.010</td>
</tr>
<tr>
<td>D</td>
<td>Min. 2.950 Max. 3.050</td>
<td>Min. 0.116 Max. 0.120</td>
</tr>
<tr>
<td>D2</td>
<td>Option 1 Min. 2.300 Max. 2.650</td>
<td>Min. 0.091 Max. 0.104</td>
</tr>
<tr>
<td></td>
<td>Option 2 Min. 1.970 Max. 2.070</td>
<td>Min. 0.078 Max. 0.081</td>
</tr>
<tr>
<td>E</td>
<td>Min. 2.950 Max. 3.050</td>
<td>Min. 0.116 Max. 0.120</td>
</tr>
<tr>
<td>E2</td>
<td>Option 1 Min. 1.400 Max. 1.750</td>
<td>Min. 0.055 Max. 0.069</td>
</tr>
<tr>
<td></td>
<td>Option 2 Min. 1.160 Max. 1.260</td>
<td>Min. 0.046 Max. 0.050</td>
</tr>
<tr>
<td>e</td>
<td>Min. 0.450 Max. 0.450</td>
<td>Min. 0.018 Max. 0.018</td>
</tr>
<tr>
<td>L</td>
<td>Min. 0.350 Max. 0.450</td>
<td>Min. 0.014 Max. 0.018</td>
</tr>
</tbody>
</table>

W-Type 12L DFN 3x3 Package

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